

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A display device comprising:

a pixel portion; and

a source signal line driving circuit[[;]] comprising:

~~a pixel portion;~~

~~a shift register included in said source signal line driving circuit for outputting a pulse signal in accordance with clock signals;~~

a current source configured to decide whether a current is outputted or not by the signal from the shift register; and

~~a level shifter to which the current is supplied from the current source included in said source signal line driving circuit for converting a voltage amplitude of input signals; and~~

~~a current source configured such that supplying a current to said level shifter is controlled based on the pulse from the shift register.~~

2. (Previously Presented) A display device according to claim 1, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

3. (Previously Presented) A display device according to claim 1, wherein said driving circuit and said pixel portion are provided over a same substrate.

4. (Previously Presented) A display device according to claim 1, wherein said driving circuit and said pixel portion are provided over different substrates.

5. (Previously Presented) A display device according to claim 1, wherein said display device is a liquid crystal display device.

6. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a personal computer.

7. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a portable information terminal.

8. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a car audio set.

9. (Previously Presented) A display device according to claim 1, wherein said display device is incorporated into a digital camera.

10. (Currently Amended) A display device comprising:
a pixel portion; and
a source signal line driving circuit[[;]] comprising:
~~a pixel portion;~~
first to x-th (x: natural number, $x \geq 2$) units, each of the first to x-th units
comprising: ~~included in said source signal line driving circuit;~~
a plurality of stages of a shift registers register ~~included in the a-th~~
~~(a: natural number, $1 \leq a \leq x$)~~ unit for outputting a pulse signal in accordance with clock signals;

a current source configured to decide whether a current is outputted or not by the signal from the plurality of stages of the shift register; and
a plurality of level shifters to which the current is supplied from the current source ~~included in said a-th unit for converting a voltage amplitude of input signals; and~~
~~an a-th current source configured such that supplying a current to said plurality of level shifters is controlled based on the pulse from the shift registers.~~

11. (Previously Presented) A display device according to claim 10, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

12. (Previously Presented) A display device according to claim 10, wherein said driving circuit and said pixel portion are provided over a same substrate.

13. (Previously Presented) A display device according to claim 10, wherein said driving circuit and said pixel portion are provided over different substrates.

14. (Previously Presented) A display device according to claim 10, wherein said display device is a liquid crystal display device.

15. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a personal computer.

16. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a portable information terminal.

17. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a car audio set.

18. (Previously Presented) A display device according to claim 10, wherein said display device is incorporated into a digital camera.

19.-36. (Canceled)

37. (Currently Amended) A display device comprising:

a pixel portion; and

a gate signal line driving circuit~~[[;]]~~ comprising:

~~a pixel portion;~~

~~a shift register included in said gate signal line driving circuit for outputting~~
a pulse signal in accordance with clock signals;

~~a current source configured to decide whether a current is outputted or not~~
by the signal from the shift register; and

~~a level shifter to which the current is supplied from the current source~~
~~included in said gate signal line driving circuit for converting a voltage amplitude of input~~
~~signals; and~~

~~a current source configured such that supplying a current to said level~~
~~shifter is controlled based on the pulse from the shift register.~~

38. (Previously Presented) A display device according to claim 37, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

39. (Previously Presented) A display device according to claim 37, wherein said driving circuit and said pixel portion are provided over a same substrate.

40. (Previously Presented) A display device according to claim 37, wherein said driving circuit and said pixel portion are provided over different substrates.

41. (Previously Presented) A display device according to claim 37, wherein said display device is a liquid crystal display device.

42. (Previously Presented) A display device according to claim 37, wherein said display device is incorporated into a personal computer.

43. (Previously Presented) A display device according to claim 37, wherein said display device is incorporated into a portable information terminal.

44. (Previously Presented) A display device according to claim 37, wherein said display device is incorporated into a car audio set.

45. (Previously Presented) A display device according to claim 37, wherein said display device is incorporated into a digital camera.

46. (Currently Amended) A display device comprising:

a pixel portion; and

a gate signal line driving circuit~~[[;]]~~ comprising:

~~a pixel portion;~~

first to y-th (y: natural number, $y \geq 2$) units, each of the first to y-th units comprising: included in said gate signal line driving circuit;

a plurality of stages of a shift registers register ~~included in the d-th~~
(~~d: natural number, $1 \leq d \leq y$~~) unit for outputting a pulse signal in accordance with clock signals;

a current source configured to decide whether a current is outputted or not by the signal from the plurality of stages of the shift register; and

a plurality of level shifters to which the current is supplied from the current source ~~included in said d-th unit for converting a voltage amplitude of input signals; and~~

~~a d-th current source configured such that supplying a current to said plurality of level shifters is controlled based on the pulse from the shift registers.~~

47. (Previously Presented) A display device according to claim 46, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

48. (Previously Presented) A display device according to claim 46, wherein said driving circuit and said pixel portion are provided over a same substrate.

49. (Previously Presented) A display device according to claim 46, wherein said driving circuit and said pixel portion are provided over different substrates.

50. (Previously Presented) A display device according to claim 46, wherein said display device is a liquid crystal display device.

51. (Previously Presented) A display device according to claim 46, wherein said display device is incorporated into a personal computer.

52. (Previously Presented) A display device according to claim 46, wherein said display device is incorporated into a portable information terminal.

53. (Previously Presented) A display device according to claim 46, wherein said display device is incorporated into a car audio set.

54. (Previously Presented) A display device according to claim 46, wherein said display device is incorporated into a digital camera.

55.-72. (Canceled)

73. (Currently Amended) A display device comprising:

a pixel portion; and

a source signal line driving circuit[;]] comprising:

~~a pixel portion;~~

~~a decoder included in said source signal line driving circuit for outputting a pulse signal~~ in accordance with input signals;

a current source configured to decide whether a current is outputted or not by the signal from the decoder; and

a level shifter to which the current is supplied from the current source ~~included in said source signal line driving circuit for converting a voltage amplitude of the input signals; and~~

~~a current source configured such that supplying a current to said level shifter is controlled based on the pulse from the decoder.~~

74. (Previously Presented) A display device according to claim 73, wherein said source signal line driving circuit and said pixel portion are provided over a member

selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

75. (Previously Presented) A display device according to claim 73, wherein said driving circuit and said pixel portion are provided over a same substrate.

76. (Previously Presented) A display device according to claim 73, wherein said driving circuit and said pixel portion are provided over different substrates.

77. (Previously Presented) A display device according to claim 73, wherein said display device is a liquid crystal display device.

78. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a personal computer.

79. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a portable information terminal.

80. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a car audio set.

81. (Previously Presented) A display device according to claim 73, wherein said display device is incorporated into a digital camera.

82. (Currently Amended) A display device comprising:

a pixel portion; and

a source signal line driving circuit[[:]] comprising:

~~a pixel portion;~~

first to x-th (x: natural number, $x \geq 2$) units, each of the first to x-th units comprising: ~~included in said source signal line driving circuit;~~

a plurality of stages of decoders a decoder ~~included in the a-th (a: natural number, $1 \leq a \leq x$) unit~~ for outputting a pulse signal in accordance with input signals;

a current source configured to decide whether a current is outputted or not by the signal from the plurality of stages of the decoder; and

a plurality of level shifters to which the current is supplied from the current source ~~included in said a-th unit for converting a voltage amplitude of the input signals; and~~

~~an a-th current source configured such that supplying a current to said plurality of level shifters is controlled based on the pulse from the decoders.~~

83. (Previously Presented) A display device according to claim 82, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

84. (Previously Presented) A display device according to claim 82, wherein said driving circuit and said pixel portion are provided over a same substrate.

85. (Previously Presented) A display device according to claim 82, wherein said driving circuit and said pixel portion are provided over different substrates.

86. (Previously Presented) A display device according to claim 82, wherein said display device is a liquid crystal display device.

87. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a personal computer.

88. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a portable information terminal.

89. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a car audio set.

90. (Previously Presented) A display device according to claim 82, wherein said display device is incorporated into a digital camera.

91.-108. (Canceled)

109. (Currently Amended) A display device comprising:

a pixel portion; and

a gate signal line driving circuit[[:]] comprising:

~~a pixel portion;~~

~~a decoder included in said gate signal line driving circuit for outputting a pulse signal~~ in accordance with input signals;

a current source configured to decide whether a current is outputted or not by the signal from the decoder; and

a level shifter to which the current is supplied from the current source
~~included in said gate signal line driving circuit for converting a voltage amplitude of the input signals; and~~

~~a current source configured such that supplying a current to said level shifter is controlled based on the pulse from the decoder.~~

110. (Previously Presented) A display device according to claim 109, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

111. (Previously Presented) A display device according to claim 109, wherein said driving circuit and said pixel portion are provided over a same substrate.

112. (Previously Presented) A display device according to claim 109, wherein said driving circuit and said pixel portion are provided over different substrates.

113. (Previously Presented) A display device according to claim 109, wherein said display device is a liquid crystal display device.

114. (Previously Presented) A display device according to claim 109, wherein said display device is incorporated into a personal computer.

115. (Previously Presented) A display device according to claim 109, wherein said display device is incorporated into a portable information terminal.

116. (Previously Presented) A display device according to claim 109, wherein said display device is incorporated into a car audio set.

117. (Previously Presented) A display device according to claim 109, wherein said display device is incorporated into a digital camera.

118. (Currently Amended) A display device comprising:
a pixel portion; and

a gate signal line driving circuit~~[[;]]~~ comprising:

~~a pixel portion;~~

first to y-th (y: natural number, $y \geq 2$) units, each of the first to y-th units
comprising: included in said gate signal line driving circuit;

a plurality of stages of a decoder ~~decoders included in the d-th (d:~~
natural number, $1 \leq d \leq y$) unit for outputting a pulse signal in accordance with input
signals;

a current source configured to decide whether a current is
outputted or not by the signal from the plurality of stages of the decoder; and

a plurality of level shifters to which the current is supplied from the
current source ~~included in said d-th unit for converting a voltage amplitude of the input~~
~~signals; and~~

~~a d-th current source configured such that supplying a current to said plurality of~~
~~level shifters is controlled based on the pulse from the decoders.~~

119. (Previously Presented) A display device according to claim 118, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

120. (Previously Presented) A display device according to claim 118, wherein said driving circuit and said pixel portion are provided over a same substrate.

121. (Previously Presented) A display device according to claim 118, wherein said driving circuit and said pixel portion are provided over different substrates.

122. (Previously Presented) A display device according to claim 118, wherein said display device is a liquid crystal display device.

123. (Previously Presented) A display device according to claim 118, wherein said display device is incorporated into a personal computer.

124. (Previously Presented) A display device according to claim 118, wherein said display device is incorporated into a portable information terminal.

125. (Previously Presented) A display device according to claim 118, wherein said display device is incorporated into a car audio set.

126. (Previously Presented) A display device according to claim 118, wherein said display device is incorporated into a digital camera.

127.-144. (Canceled)

145. (Currently Amended) A semiconductor device comprising:
a shift register for outputting a pulse signal in accordance with clock signals;
a current source configured to decide whether a current is outputted or not by the
signal from the shift register; and
a level shifter to which the current is supplied from the current source for
~~converting a voltage amplitude of input signals; and~~
~~a current source configured such that supplying a current to said level shifter is~~
~~controlled based on the pulse from the shift register.~~

146. (Previously Presented) A semiconductor device according to claim 145, wherein said driving circuit is provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

147. (Previously Presented) A semiconductor device according to claim 145, wherein said semiconductor device is a liquid crystal display device.

148. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a personal computer.

149. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a portable information terminal.

150. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a car audio set.

151. (Previously Presented) A semiconductor device according to claim 145, wherein said display device is incorporated into a digital camera.

152. (Currently Amended) A semiconductor device comprising:

first to x-th (x : natural number, $x \geq 2$) units[;], each of the first to x-th units comprising:

a plurality of stages of a shift registers register ~~included in the a-th (a: natural number, $1 \leq a \leq x$) unit~~ for outputting a pulse signal in accordance with clock signals;

a current source configured to decide whether a current is outputted or not by the signal from the plurality of stages of the shift register; and

~~a plurality of level shifters included in said a-th unit for converting a voltage amplitude of input signals; and~~

~~an a-th current source configured such that supplying a current to said plurality of level shifters is controlled based on the pulse from the shift registers.~~

153. (Previously Presented) A semiconductor device according to claim 152, wherein said driving circuit is provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

154. (Previously Presented) A semiconductor device according to claim 152, wherein said semiconductor device is a liquid crystal display device.

155. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a personal computer.

156. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a portable information terminal.

157. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a car audio set.

158. (Previously Presented) A semiconductor device according to claim 152, wherein said display device is incorporated into a digital camera.

159. (Currently Amended) A semiconductor device comprising:

a driving circuit comprising:

a decoder ~~included in said driving circuit~~ for outputting a pulse signal in accordance with input signals;

a current source configured to decide whether a current is outputted or not by the signal from the plurality of stages of the decoder; and

a level shifter to which the current is supplied from the current source
~~included in said driving circuit for converting a voltage amplitude of the input signals;~~
and

~~a current source configured such that supplying a current to said level shifter is
controlled based on the pulse from the decoder.~~

160. (Previously Presented) A semiconductor device according to claim 159,
wherein said driving circuit is provided over a member selected from the group
consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a
single crystal wafer.

161. (Previously Presented) A semiconductor device according to claim 159,
wherein said semiconductor device is a liquid crystal display device.

162. (Previously Presented) A semiconductor device according to claim 159,
wherein said display device is incorporated into a personal computer.

163. (Previously Presented) A semiconductor device according to claim 159,
wherein said display device is incorporated into a portable information terminal.

164. (Previously Presented) A semiconductor device according to claim 159,
wherein said display device is incorporated into a car audio set.

165. (Previously Presented) A semiconductor device according to claim 159,
wherein said display device is incorporated into a digital camera.

166. (Currently Amended) A semiconductor device comprising:
a driving circuit~~[[;]]~~ comprising:

first to x-th (x : natural number, $x \geq 2$) units, each of the first to x-th units comprising: included in said driving circuit;

a plurality of stages of a decoder ~~decoders included in the a-th~~ (a : natural number, $1 \leq a \leq x$) unit for outputting a pulse signal in accordance with input signals;

a current source configured to decide whether a current is outputted or not by the signal from the plurality of stages of the decoder; and

a plurality of level shifters to which the current is supplied from the current source ~~included in said a-th unit for converting a voltage amplitude of the input signals; and~~

~~an a-th current source configured such that supplying a current to said plurality of level shifters is controlled based on the pulse from the decoders.~~

167. (Previously Presented) A semiconductor device according to claim 166, wherein said driving circuit is provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

168. (Previously Presented) A semiconductor device according to claim 166, wherein said semiconductor device is a liquid crystal display device.

169. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a personal computer.

170. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a portable information terminal.

171. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a car audio set.

172. (Previously Presented) A semiconductor device according to claim 166, wherein said display device is incorporated into a digital camera.

173. (Previously Presented) A display device according to claim 1, wherein said source signal line driving circuit comprises thin film transistors.

174. (Previously Presented) A display device according to claim 10, wherein said source signal line driving circuit comprises thin film transistors.

175. (Previously Presented) A display device according to claim 37, wherein said gate signal line driving circuit comprises thin film transistors.

176. (Previously Presented) A display device according to claim 46, wherein said gate signal line driving circuit comprises thin film transistors.

177. (Previously Presented) A display device according to claim 73, wherein said source signal line driving circuit comprises thin film transistors.

178. (Previously Presented) A display device according to claim 82, wherein said source signal line driving circuit comprises thin film transistors.

179. (Previously Presented) A display device according to claim 109, wherein said gate signal line driving circuit comprises thin film transistors.

180. (Previously Presented) A display device according to claim 118, wherein said gate signal line driving circuit comprises thin film transistors.

181. (Previously Presented) A semiconductor device according to claim 145, wherein said driving circuit comprises thin film transistors.

182. (Previously Presented) A semiconductor device according to claim 152, wherein said driving circuit comprises thin film transistors.

183. (Previously Presented) A semiconductor device according to claim 159, wherein said driving circuit comprises thin film transistors.

184. (Previously Presented) A semiconductor device according to claim 166, wherein said driving circuit comprises thin film transistors.

185. (Currently Amended) A semiconductor device comprising:
a shift register for outputting a pulse signal in accordance with clock signals;
a current source configured to decide whether a current is outputted or not by the signal from the shift register;
a level shifter to which the current is supplied from the current source for
converting a voltage amplitude of input signals; and
~~a current source configured such that supplying a current to said level shifter is~~
~~controlled based on the pulse from the shift register; and~~
a latch circuit into which output signals of said level shifter are inputted.

186. (Currently Amended) A display device comprising:
a shift register for outputting a pulse signal in accordance with clock signals;
a current source configured to decide whether a current is outputted or not by the signal from the shift register;

a level shifter to which the current is supplied from the current source for
~~converting a voltage amplitude of input signals; and~~
~~a current source configured such that supplying a current to said level shifter is~~
~~controlled based on the pulse from the shift register; and~~
a latch circuit into which output signals of said level shifter are inputted.

187. (Currently Amended) A semiconductor device comprising:
a shift register for outputting a pulse signal in accordance with clock signals;
a current source configured to decide whether a current is outputted or not by the
signal from the shift register;

a level shifter to which the current is supplied from the current source for
~~converting a voltage amplitude of input signals;~~
~~a current source configured such that supplying a current to said level shifter is~~
~~controlled based on the pulse from the shift register;~~
a first latch circuit into which output signals of said level shifter are inputted; and
a second latch circuit into which output signals of said first latch circuit are
inputted.

188. (Currently Amended) A display device comprising:
a shift register for outputting a pulse signal in accordance with clock signals;
a current source configured to decide whether a current is outputted or not by the
signal from the shift register;

a level shifter to which the current is supplied from the current source for
~~converting a voltage amplitude of input signals;~~
~~a current source configured such that supplying a current to said level shifter is~~
~~controlled based on the pulse from the shift register;~~
a first latch circuit into which output signals of said level shifter are inputted; and

a second latch circuit into which output signals of said first latch circuit are inputted.